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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO
09/750,400	12/28/2000	Zhiliang Julian Chen	TI-31655	6930
7590 10/09/2003			EXAMINER	
W. James Brady III			FRECH, KARL D	
Texas Instruments Incorporated, P.O. Box 655474, M/S 3999			ART UNIT	PAPER NUMBER
Dallas, TX 75265			2876	

DATE MAILED: 10/09/2003

Please find below and/or attached an Office communication concerning this application or proceeding.

		y				
	Application No.	Applicant(s)				
	09/750,400	CHEN ET AL.				
Office Action Summary	Examiner	Art Unit				
	Karl D Frech	2876				
The MAILING DATE of this communication appears on the cover sheet with the correspondence address						
Period for Reply						
A SHORTENED STATUTORY PERIOD F THE MAILING DATE OF THIS COMMUNI - Extensions of time may be available under the provisions after SIX (6) MONTHS from the mailing date of this comm - If the period for reply specified above is less than thirty (3 - If NO period for reply is specified above, the maximum st - Failure to reply within the set or extended period for reply - Any reply received by the Office later than three months a earned patent term adjustment. See 37 CFR 1.704(b). Status	ICATION. of 37 CFR 1.136(a). In no event, however nunication. It is a reply within the statutory minimatutory period will apply and will expire SIX will, by statute, cause the application to be	er, may a reply be timely filed um of thirty (30) days will be considered timely. K (6) MONTHS from the mailing date of this communication. ecome ABANDONED (35 U.S.C. § 133).				
1) Responsive to communication(s) fil	ed on					
2a) ☐ This action is FINAL .	2b)⊠ This action is non-fina	al.				
3) Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under <i>Ex parte Quayle</i> , 1935 C.D. 11, 453 O.G. 213. Disposition of Claims						
4) Claim(s) 1-25 is/are pending in the	application.	· ·				
4a) Of the above claim(s) is/a	re withdrawn from considerat	ion.				
5) Claim(s) is/are allowed.						
6)⊠ Claim(s) <u>1-25</u> is/are rejected.						
7) Claim(s) is/are objected to.						
8) Claim(s) are subject to restrict Application Papers	ction and/or election requirem	ent.				
9) The specification is objected to by the	e Examiner.					
10)⊠ The drawing(s) filed on <u>07 May 2001</u> is/are: a)⊠ accepted or b)⊡ objected to by the Examiner.						
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).						
11)☐ The proposed drawing correction file	d on is: a)□ approved	b) disapproved by the Examiner.				
If approved, corrected drawings are required in reply to this Office action.						
12)☐ The oath or declaration is objected to by the Examiner.						
Priority under 35 U.S.C. §§ 119 and 120						
13) Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).						
a) All b) Some * c) None of:						
1. Certified copies of the priority documents have been received.						
2. Certified copies of the priority documents have been received in Application No						
	national Bureau (PCT Rule 17					
14) ☐ Acknowledgment is made of a claim f	or domestic priority under 35	U.S.C. § 119(e) (to a provisional application).				
a) ☐ The translation of the foreign lar						
Attachment(s)						
 Notice of References Cited (PTO-892) Notice of Draftsperson's Patent Drawing Review (PTO-1449) Information Disclosure Statement(s) (PTO-1449) 	PTO-948) 5) 🔲 N	nterview Summary (PTO-413) Paper No(s) Notice of Informal Patent Application (PTO-152) Other:				

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1. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless -

(b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.

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- 2. Claims 1-4,8-12,16-25 are rejected under 35 U.S.C. 102(b) as being anticipated by Wei et al 4,672,412. Wei discloses an image sensor (CCD) with a plurality of rows of pixels and associated circuits (first control inputs) and a plurality of columns and associated circuits (second control inputs). It is disclosed in column 9 lines 18+ that the CCD is sequentially reset row-by-row, column by column (resetting segments without resetting the entire CCD at one time). Wei discloses in column 4 lines 3-33 that there are separately addressable line segments within the individual rows of the CCD.
- 3. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:
 - (a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negatived by the manner in which the invention was made.
- 4. This application currently names joint inventors. In considering patentability of the claims under 35 U.S.C. 103(a), the examiner presumes that the subject matter of the various claims was commonly owned at the time any inventions covered therein were made absent any evidence to the contrary. Applicant is advised of the obligation under 37 CFR 1.56 to point out the inventor and invention dates of each claim that was not commonly owned at the time a later invention was made in order for the examiner to

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consider the applicability of 35 U.S.C. 103(c) and potential 35 U.S.C. 102(e), (f) or (g) prior art under 35 U.S.C. 103(a). Claims 5-7,13-15 are rejected under 35 U.S.C. 103(a) as being unpatentable over Wei et al 4,672,412. Wei discloses that which is seen above. Wei does not specifically disclose logic gates. However, Wei discloses in column 2 line 12 that transfer gates are used. Logic gates are old and well known. It would have been obvious to a person of ordinary skill in the art at the time the invention was made to use logic gates as the gates of Wei in order to allow for binary manipulation of the signals and for saturation considerations.

5. The prior art made of record and not relied upon is considered pertinent to applicant's disclosure. Kozlowski et al 6,535,247 and Janesick 5,844,598 both disclose reset properties of two-dimensional imaging arrays.

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Karl D Frech whose telephone number is (703) 305 3491. The examiner can normally be reached on maxi-flex.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Michael G Lee can be reached on (703) 305 3503. The fax phone number for the organization where this application or proceeding is assigned is (703) 872-9306.

Any inquiry of a general nature or relating to the status of this application or proceeding should be directed to the receptionist whose telephone number is (703) 308 0956.

Primary Examiner

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